

# *Inside the Machine*

## An Illustrated Introduction to Microprocessors and Computer Architecture

by Jon Stokes

errata updated to print 6

Page	Error				Correction				Print corrected
1	All of them rely on a limited repertoire of technologies that enable them do the myriad kinds of miracles we've come to expect from them.				All of them rely on a limited repertoire of technologies that enable them <b>to</b> do the myriad kinds of miracles we've come to expect from them.				Print 3
93		Pentium Pro Vitals	Pentium II Vitals	Pentium III Vitals					Print 2
	<b>Introduction Date</b>	November 1, 1995	May 7, 1997	February 26, 1999					
	<b>Process</b>	0.60/0.35 micron	0.35 micron	0.25 micron					
	<b>Transistor Count</b>	5.5 million	7.5 million	9.5 million					
	<b>Clock Speed at Introduction</b>	150, 166, 180, and 200 MHz	233, 266, and 300 MHz	450 and 500 MHz					
	<b>L1 Cache Size</b>	8KB instruction, 8KB data	16KB instruction, 16KB data	16KB instruction, 16KB data					
	<b>L2 Cache Size</b>	<b>256KB or 512KB (on-die)</b>	512KB (off-die)	<b>512KB (on-die)</b>					
	<b>x86 ISA Extensions</b>		MMX	SSE added in 1999					